

## IBM posts SPEC CPU2006 scores for x3500 M3

*x3500 M3 achieves outstanding processor performance for compute-intensive applications*

February 15, 2011 ... IBM® today announces SPEC® CPU2006 benchmark scores for the IBM System x® 3500 M3 server using the Intel® Xeon® 5600 Processor Series.

The x3500 M3 delivered competitive scores with two Intel Xeon X5690 processors (3.46GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/12 cores/24 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux® Enterprise Server 11 x64. (1)

The x3500 M3 delivered competitive scores with two Intel Xeon X5687 processors (3.60GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/8 cores/16 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux Enterprise Server 11 x64. (2)

The scores in the following table are the first SPEC CPU2006 results published for these x3500 M3 processor models.

<b>SPEC CPU2006 Benchmark</b>	<b>Six-Core Intel Xeon Processor X5690 – 3.46GHz</b>	<b>Quad-Core Intel Xeon Processor X5687 – 3.60GHz</b>
SPECint®2006	45.8	47.0
SPECint_base2006	43.0	43.9
SPECint_rate2006	413	325
SPECint_rate_base2006	387	307
SPECfp®2006	63.5	65.9
SPECfp_base2006	59.6	61.7
SPECfp_rate2006	271	238
SPECfp_rate_base2006	263	231

IBM System x3500 M3 servers are self-contained, high-performance, 5U tower (optional rack-mounted) systems designed for Web and business server applications in remote or distributed environments. They are highly scalable in configuration, performance, and availability. These flexible tower models deliver the best blend of power, manageability, expandability, and serviceability. They meet the requirements of server applications in the small-to-mainstream businesses that need an affordable, general-purpose network server that fits into business environments where tower configurations are required.

Results are current as of February 15, 2011. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at <http://www.spec.org/cpu2006/results/>.

(1) The x3500 M3 model using the Intel Xeon Processor X5690 is planned to be generally available March 15, 2011. The x3500 M3 as configured for this benchmark will be available March 15, 2011.

(2) The x3500 M3 model using the Intel Xeon Processor X5687 is planned to be generally available March 15, 2011. The x3500 M3 as configured for this benchmark will be available March 15, 2011.

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